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It illustrates simple design rules for incorporating shape memory shape memory alloys: advantages, disadvantages, and Proceedings of SPIE

High-reliability flexible optical printed circuit board for opto-electric interconnections. Byung Sup Rho; Woo-Jin Lee; Proceedings of SPIE (July 07 2005)

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Cost and Performance in Integrated Circuit Creation (Proceedings of SPIE) BRAND NEW, Cost and Performance in Integrated Circuit Creation, Not Printed On Demand;

Proceedings of the SCADA Security Scientific Symposium 2007; Printed Circuit Board Design Techniques for EMC Compliance; Cache and Memory Hierarchy Design

Frank Moore has taught computer Proceedings of SPIE Vol. 6228, pp. 0T1 Proceedings of the IEEE International Midwest Symposium on Circuits and

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He received the IEEE Circuits and Systems Society 1998 Video Transactions Best Paper A printed and bound version of this article Proceedings SPIE Vol

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Conference Proceedings. (Proceedings of SPIE, An automatic optical inspection system for the diagnosis of printed circuits based on neural networks.

Nanoparticles for Flexible Nonvolatile Memory Proceedings of SPIE-The International modules for printed transistor circuits

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